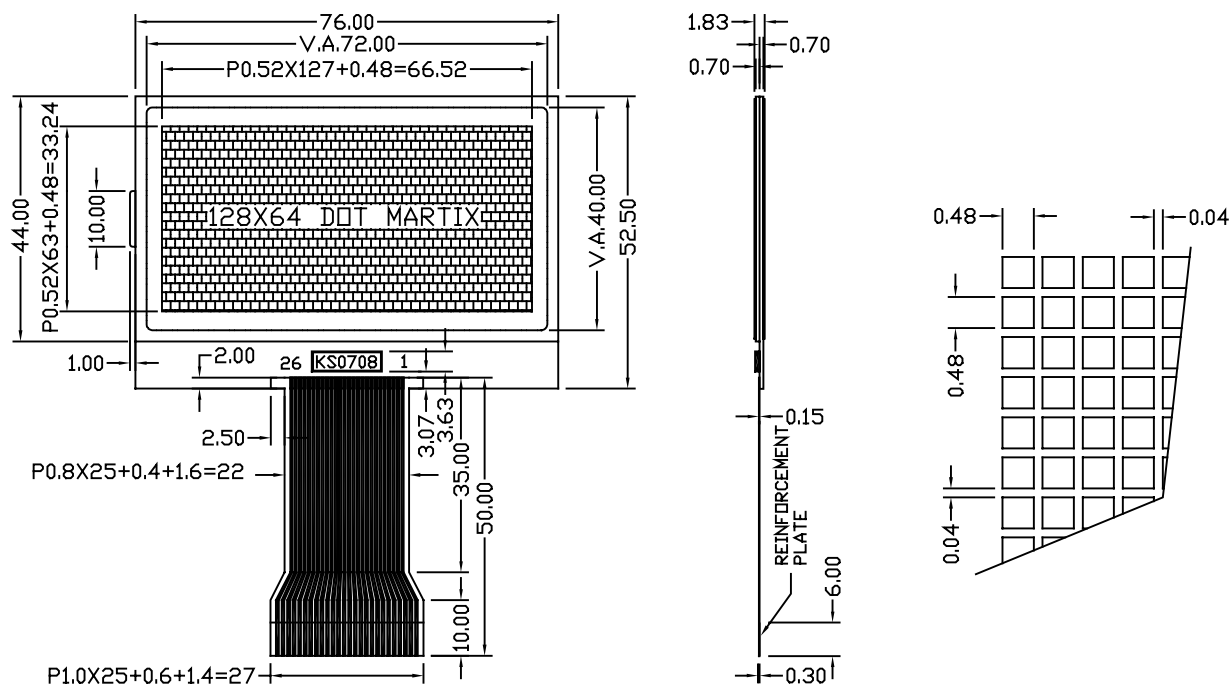


① EXTERNAL DIMENSION



② MECHANICAL DATA

ITEM	SPECIFICATION	UNIT
Module Size (W X H X T)	76.0 X 52.5 X 1.83 (without FPC)	mm
Viewing Area (W X H)	72.0 X 40.0	mm
Number of Dots (W X H)	128.0 X 64.0	dots
Dot Pitch (W X H)	0.52 X 0.52	mm
Dot Size (W X H)	0.48 X 0.48	mm
Weight	15.5	g

③ PIN CONFIGURATION

ITEM	SYMBOL	DESCRIPTION
1	VEE	Power supply for LCD drive circuit .
2	V5	LCD driver supply voltages. The voltages must satisfy the following relationship: $VDD \geq V1 \geq V2 \geq V3 \geq V4 \geq V5 \geq VEE$
3	V4	
4	V3	
5	V2	
6	V1	
7	VDD	Power supply for logic circuit and LCD.
8	VSS	0V(GND).
9	PCLK2	Phase of internal shift clock (CLK2) H : Data shift at the rising edge of CLK2 L : Data shift at the falling edge of CLK2
10	CS2B	Second chip (S65 ~ S128) select input.
11	C	RC Oscillator
12	CR	
13	R	
14	DB0	Data I/O pins for display memory.
15	DB1	
16	DB2	
17	DB3	
18	DB4	
19	DB5	
20	DB6	
21	DB7	
22	RS	Register selection.
23	RW	Read or Write.
24	E	Enable signal.
25	CS1B	First chip (S1 ~ S64) select input.
26	RESETB	Reset input (Chip is initialized when RESETB is "LOW")

④ BLOCK DIAGRAM

